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System and method of voice recognition near a wireline node of a network supporting cable television and/or video delivery

T Calderone, PM Cock... - US Patent App. 11/283,176, 2005 - Google Patents

... 13,2006 (54) SYSTEM AND METHOD OF VOICE RECOGNITION **NEAR** A WIRELINE NODE OF A NETWORK SUPPORTING CABLE TELEVISION ... 1 ODD ^'00 1100 J 1200% D'DL I I _T1300, HFCoax — OPTICAL NODE SPEECH RECOGNITION **PROCESSOR** SYSTEM 3200 ...[Related articles](#) - [All 6 versions](#)An overview of the BlueGene/L supercomputer[\[HTML\] from computer.org](#)NR Adiga, G Almasi, GS Almasi... - ACM/IEEE 2002 ..., 2002 - [ieeexplore.ieee.org](#)... The compute node kernel handles basic communication **tasks** and all the functions necessary for high ... **Table 1**: Uncorrectable hard failure Rates of BlueGene/L by major component ... with the mode specified in the PowerPC defined floating-point status and control **register** (FPSCR ...[Cited by 277](#) - [Related articles](#) - [All 6 versions](#)[\[PDF\] Redundancy for Epics IOCs](#)[\[PDF\] from cam.ch](#)M Clausen... - Proceedings of ICALPCS07, Knoxville, USA - [accelconf.web.cern.ch](#)... In case of redundancy the PRR calls rmtRegisterDriver() with the address of the entry **table** as an argument. ... Scan **tasks** The periodic scan **tasks register** normally at the RMT during their initialization. ... Under vxWorks one **task** is spawned for each state set. ...[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 2 versions](#)[\[DOC\] Redundancy for EPICS IOCs](#)[\[DOC\] from desy.de](#)CA Server - [www.mks2.desy.de](#)... In case of redundancy the PRR calls rmtRegisterDriver() with the address of the entry **table** as an argument. ... Scan **tasks**. The periodic scan **tasks register** normally at the RMT during their initialization. ... Under vxWorks one **task** is spawned for each state set. ...[Related articles](#) - [View as HTML](#) - [All 3 versions](#)Selective device reset method for device sharing with fail-over

DE Bohm, EC Kissel... - US Patent App. 11/068,399, 2004 - Google Patents

... 15,2006 (54) SELECTIVE DEVICE RESET METHOD FOR DEVICE SHARING WITH **FAIL-OVER**

(75) Inventors: David E. Bohm, Tucson, AZ (US); Erick C. Kissel, Tucson, AZ (US); Jeou ... 2006

Sheet 9 of 9 US 2006/0129666 A1 Ed Bi i CA5 O Ol > e c ^ c j j > c c ^ JT **TABLE** S O r ...[All 2 versions](#)Redundant control architecture for a network device

SN Barbas... - US Patent App. 10/011,511, 2001 - Google Patents

... The redundant control plane includes first and second Router Control **Processor** (RCP)/Bridge Hotswap Card ... **failover** or switchover respectively to a redundant bus or RCP/BHC **pair**, as the ... 6 is a **table** depicting master selection based upon the three master selection output ...

[Cited by 15](#) - [Related articles](#) - [All 4 versions](#)

Method and system for selecting a master controller in a redundant control plane having plural controllers

RJ Walsh, SN Barbas... - US Patent App. 09/999,156, 2001 - Google Patents

... [0008] For these reasons, it would be desirable to have the ability to reliably select a control **processor** from a **pair** of redundant control processors for a control plane in a network device. ...

The at least one identification signal is based upon a predetermined truth **table**. ...

[Cited by 5](#) - [Related articles](#) - [All 4 versions](#)

Task-based Dynamic Fault Tolerance for Humanoid Robot Applications and Its Hardware Implementation

[\[PDF\] from academypublisher.com](#)

M Murakami - Journal of Computers, 2008 - ojs.academypublisher.com

... For example, the addition of a duplicate CPU for fault detection increases the failure rate of the unit. ... The robot based on this scheme memorizes the mode list describing the standby and safety modes for each **task**. **Table I** shows an example of the mode list. ...

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DMD: network device driver architecture for high-performance systems

M Andjelic - ... , 2003. ConTEL 2003. Proceedings of the 7th ..., 2003 - ieeeexplore.ieee.org

... AAr mapping, DNDD writes to NIC **register** the exact memory address of queues. ... Extending support for another NIC types is easier **task** since DKA and DUL containing core logic for tunneled mode of operation are fully reused. IV. ... 629 Page 6. **Table I** -Measurement Results ...

[Cited by 1](#) - [Related articles](#)

Data synchronization for system controllers

FLGG Vavoven - US Patent 7,363,531, 2006 - Google Patents

... 65 information, for example in the form of sequential key/value **pairs** for a hash **table**. ... a data switch (DX) 78' which can multi-**processor** shared-memory system, the **task** of keeping ... the two all the difference caches within the system coherent requires **Processor pairs** and between ...

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[Scheduling algorithms for multiprogramming in a hard-real-time environment](#)[\[PDF from cs.cmu.edu\]](#)

CL Liu... - Journal of the ACM (JACM), 1973 - portal.acm.org

... 3. For a set of **two tasks** with fixed priority assignment, the least upper bound to the **processor** utilization factor is $U \leq 1$. Let r_1 and r_2 be **two tasks** with their periods being T_1 and T_2 and their run-times being C_1 and C_2 ... Journal of the **Association** for Computing Machinery, Vol. 20, No. ...

[Cited by 872](#) - [Related articles](#) - [All 104 versions](#)[On scheduling tasks with a quick recovery from failure](#)CM Krishna... - Computers, IEEE Transactions on, 2006 - [ieeexplore.ieee.org](#)

... **Tasks** are run periodically, and each release of a **task** is called a version. ... The multiple copies of versions are called clones. There are **two types** of clones: primary and ghost. ... Each ghost clone of version j has a notification time, v_j , **associated** with it. After this time, the ...

[Cited by 129](#) - [Related articles](#) - [All 9 versions](#)[Backup fault tolerant computer system](#)

SD Glazer, J Baumbach, A Borg... - US Patent 4,590,554, 1986 - Google Patents

... time of starting or from the time of the last synchro- nization had emitted **two** messages. Therefore, the **task 5 processor 22** in the course of processing, when it reaches the first time ... TABLE VI 20 There has thus been described the fundamental oper- ations **associated** with the ...

[Cited by 101](#) - [Related articles](#) - [All 3 versions](#)[A software architecture for multidisciplinary applications: Integrating task and data parallelism](#)[\[PDF from dsc.ind](#)

B Chapman, P Mehrotra, J Van Rosendale... - ... Processing: CONPAR 94 ... 1994 - Springer

... The follow- ing code fragment declares **two** objects of type stack (see Section 3.1): SDA (stack) OF ... SDA (stack) OF TYPE(nser_type) :: user_stack The of-clause provides a type name to be **associated** with the ... Only the **task** declaring an SDA vari- able can initialize the variable. ...

[Cited by 73](#) - [Related articles](#) - [Library Search](#) - [BI Direct](#) - [All 10 versions](#)[Misconceptions about real-time computing: A serious problem for next-generation systems](#)JA Stankovic - Computer, 2002 - [ieeexplore.ieee.org](#)

... no provisions to support reasoning about time and reliability abstractions, **two** vital aspects of real ... as many jobs as possi- ble, subject to meeting the **task** timing requirements ... One primary measure of a scheduling algorithm is its **associated processor** utili- zation level below which ...

[Cited by 753](#) - [Related articles](#) - [All 4 versions](#)[Hybrid scheduling for the parallel solution of linear systems](#)[\[PDF from cs.cmu.edu\]](#)

PR Amestoy, A Guermouche, JY L'Ecuyer... - Parallel computing, 2006 - Elsevier
... to as load i , that corresponds to the computational work (number of floating-point operations)
associated with: • each **task** ... The dynamic scheduling decision taken by the master **processor**
of a type 2 **task** is then guided by its view of the workload of its candidate **processors**. ...

[Cited by 114](#) - [Related articles](#) - [All 13 versions](#)

Fault tolerant load management system and method

C Dwork, JY Halpern... - US Patent 5,727,210, 1998 - Google Patents

... The controller further includes a scheduler, which is the processing system might include **two**
or more ... 2J selected by other **processors**, are to be executed by its available **tasks** to the ... Thus,
if one of the pro- **associated processor**, and schedules the selected **tasks** for processors fails ...

[Cited by 43](#) - [Related articles](#) - [All 4 versions](#)

A combinatorial characterization of the distributed tasks which are solvable in the presence of one faulty processor

O Bray, S Moran... - Proceedings of the seventh annual ACM ..., 1988 - portal.acm.org

... (3) Order Preserving Renaming (OPR) [ABDKPR]: This **task** is defined for a given integer K , where
 $K \geq 2$. The input set X is the set of all vectors $\langle x_i, +^i \rangle$ A protocol for a given network is a set of
 N programs, each **associated with** a single **processor** in the network. ...

[Cited by 57](#) - [Related articles](#) - [Library Search](#) - [All 3 versions](#)

Reaching agreement on processor-group membership in synchronous distributed systems

[PDF from eprint](#)

F Cristian - Distributed Computing, 1991 - Springer

... that batches all "present" messages broadcast for identical view times V into one message (1.12)
containing V and the set $M = \text{MEMBERS}(V)$ of **processors** who have ... 4.1 introduces **two** kinds of
complications. A minor one is in the computation of **task** start times and delays. ...

[Cited by 283](#) - [Related articles](#) - [Library Search](#) - [All 19 versions](#)

Method and apparatus for allocating tasks to remote networked processors

T Downs... - US Patent 6,112,243, 2000 - Google Patents

... 2 illustrates a simplified block diagram of the resource allocator 14, configured in accordance
with the ... software code that under the direction of the allocation unit 36 transfers the **task** to the ...
provider 16 is accepted, the application unit 40 adds the information **associated with** the ...

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